

IN THE CLAIMS

Please amend the claims as follows.

A1 9. (amended) A display according to claim 8,
characterized in that when the total number of signal lines of
said plurality of columns which is decided by a display system is
set to N, the number of said driver circuits is set to N/n.

A2 16. (amended) A display according to claim 15,
characterized in that said plurality of driver circuits have a
function to stop the operation of an output circuit of said
plurality of driver circuits for said blanking period.

Please add the following new claims.

21. (new) A display according to claim 1, characterized
in that a surplus connecting region that does not contribute to
said display portion does not occur on the said display.

A3 22. (new) A display according to claim 1, characterized
in that a driver circuit of said plurality of driver circuits is
separate and distinct from another driver circuit of said
plurality of driver circuits.

23. (new) A display according to claim 3, characterized in that a surplus connecting region that does not contribute to said display portion does not occur on the said display.

24. (new) A display according to claim 3, characterized in that a driver circuit of said plurality of driver circuits is separate and distinct from another driver circuit of said plurality of driver circuits.

25. (new) A liquid crystal display comprising:
a display portion, said display portion having a plurality of gate lines, a plurality of signal lines and a plurality of pixels, a pixel of said plurality of pixels being located at an intersection of a gate line of said plurality of gate lines and a signal line of said plurality of signal lines; and

a plurality of driver circuits, a driver circuit of said plurality of driver circuits being separate and distinct from another driver circuit of said plurality of driver circuits, each driver circuit of said plurality of driver circuits having a plurality of output terminals, said plurality of output terminals providing a plurality of signal potentials to a group of signal lines of said plurality of signal lines, said group of signal lines being less than all of said plurality of

signal lines.

26. (new) A display according to claim 25, wherein said each driver circuit is separate and distinct from another driver circuit of said plurality of driver circuits.

27. (new) A display according to claim 25, wherein said plurality of pixels are arranged in a two-dimensional matrix shape.

93 28. (new) A display according to claim 25, wherein said pixel of said plurality of pixels includes a transistor, a gate electrode of said transistor being electrically connected to said gate line, a source/drain of said transistor being electrically connected to said signal line.

29. (new) A display according to claim 25, wherein said plurality of gate lines is a plurality of rows and said plurality of signal lines is a plurality of columns.

30. (new) A display according to claim 25, wherein an output terminal of said plurality of output terminals provides a signal potential of said plurality of signal potentials to said signal line.

31. (new) A display according to claim 25, wherein a surplus connecting region that does not contribute to said display portion does not occur on the said display.

32. (new) A display according to claim 25, wherein an amount of signal lines of said plurality of signal lines is connected to said driver circuit, said amount being an integer not less than 1.

AB 33. (new) A display according to claim 32, wherein another amount of signal lines of said plurality of signal lines is connected to said another driver circuit, said amount being equal to said another amount, said another amount being an integer not less than 1.

34. (new) A display according to claim 32, wherein a remainder amount of said plurality of signal lines is less than said amount of said plurality of signal lines, said remainder amount being the amount of said plurality of signal lines connected to a remainder driver circuit of said plurality of driver circuits, each other driver circuit of said plurality of driver circuits being connected to said amount of said plurality of signal lines.

35. (new) A display according to claim 34, wherein another amount of said output terminals is the amount of said output terminals for said another driver circuit, and said amount is equal to said another amount.

36. (new) A display according to claim 35, wherein the aggregate number of output terminals of said plurality of driver circuits is set to a measure of the total number of said signal lines.

AB ^{SUB} C2 37. (new) A display according to claim 36, wherein an output terminal of said plurality of output terminals is electrically connected to an input terminal of a time-divisional switch, said time-divisional switch providing a signal potential of said plurality of signal potentials to said signal line as a de-multiplexed signal potential.

SUB B2 38. (new) A display according to claim 35, wherein said time-division switch distributes said plurality of signal potentials as a red signal potential, a green signal potential and a blue signal potential.

39. (new) A display according to claim 38, wherein said

AB red signal potential is applied to a red pixel of said plurality of pixels, said green signal potential is applied to a green pixel of said plurality of pixels and said blue signal potential is applied to a blue pixel of said plurality of pixels.
